

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

FUJIMOTO

Atty. Ref.: 1035-456

Serial No. To be Assigned

Group:

Filed: July 1, 2003

Examiner:

For: SAMPLE-AND-HOLD AMPLIFIER CIRCUIT AND
PIPELINED A/D AND D/A CONVERTERS USING SAMPLE
HOLD AMPLIFICATION CIRCUIT

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July 1, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

- | | | |
|-------------------------------------|--|----------|
| <input checked="" type="checkbox"/> | 1. PTO-1449 Pursuant to 37 CFR 1.97(b) [within 3 months of filing or prior to 1st Office Action] | N/C |
| <input type="checkbox"/> | 2. Statement Pursuant to 37 CFR 1.97(c) [before Final Office Action and Allowance] | N/C |
| <input type="checkbox"/> | 3. Fee Payment Pursuant to 37 CFR 1.97(c) [before Final Office Action and Allowance] | \$180.00 |
| <input type="checkbox"/> | 4. Pursuant to 37 CFR 1.97(d) [before Issue Fee payment] | \$180.00 |

The following are submitted in the above-identified application in compliance with 37 C.F.R. §§ 1.97 and 1.98:

- | | |
|-------------------------------------|---|
| <input checked="" type="checkbox"/> | 5. A list of documents on Form PTO-1449. Actual copies of the references listed on the attached form PTO-1449 are not included herewith in accordance with MPEP §609, these references being previously submitted in any and all of the parent applications. The parent application includes U.S. application Serial No. 09/932,025 |
|-------------------------------------|---|

This paper is submitted in accordance with:

- ☒ 6. 37 CFR 1.97(b): [within 3 months of filing or prior to 1st Office Action]
- ☐ 7. 37 CFR 1.97(c): [before Final Office Action and Allowance, whichever is earlier]; and
- ☐ a) The required Statement made in item 9 below; or
- ☐ b) The \$180.00 fee specified in 37 CFR §1.17(p) for submission of this Information Disclosure Statement is authorized in item 10 below.
- ☐ 8. 37 CFR §1.97(d): [before issue fee payment]; and
- ☐ a) The fee (\$180.00) required by 37 CFR §1.17(p) is authorized in item 10 below; and
- ☐ b) The required Statement is stated in item 9 below.
- ☐ 9. Statement under 37 CFR 1.97(e)
- ☐ a) The undersigned attorney of record hereby certifies under 37 C.F.R. §1.97(e) that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement (each item contained in this IDS was the first citation of that item by a foreign patent office in a counterpart foreign application which occurred no more than three months prior to the filing of this IDS); or
- ☐ b) No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement, after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement.
- ☒ 10. Please charge all applicable fees associated with the submission of this Information Disclosure Statement and any other fees applicable to this application to Deposit Account No. 14-1140. An original and one (1) copy of this document are enclosed.

Respectfully submitted,

NIXON & VANDERHYE P.C.

By: 

FUJIMOTO

Serial No. To be Assigned

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INFORMATION DISCLOSURE CITATION

ATTY. DOCKET NO.

1035-456

SERIAL NO.

To be Assigned

APPLICANT

FUJIMOTO

(Use several sheets if necessary)

FILING DATE

July 1, 2003

GROUP

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

| | |
|--|--|
| | U.S. Patent Application Serial No. 09/932,025 filed August 20, 2001 |
| | "A 14-Bit 10-MHz Calibration-Free CMOS Pipelined A/D Converter" (L.A. Singer et al, 1996 Symposium on VLSI Circuits Digest of Technical Papers, pages 94-95) |
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| | |

*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)